

Electronic Acknowledgement Receipt

EFS ID:	3739111
Application Number:	10813837
International Application Number:	
Confirmation Number:	6047
Title of Invention:	VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR
First Named Inventor/Applicant Name:	Akhil K. Garlapati
Customer Number:	22120
Filer:	Mark Zagorin/Katrina Prati
Filer Authorized By:	Mark Zagorin
Attorney Docket Number:	026-0044
Receipt Date:	06-AUG-2008
Filing Date:	31-MAR-2004
Time Stamp:	16:03:54
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Change of Address	026-0044_COA_20080806.pdf	93270 <small>d0540efc361e30b6c0cc3dd916b9299b41bb69f</small>	no	1

Warnings:

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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